

FIG. 1

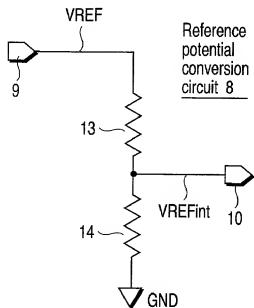
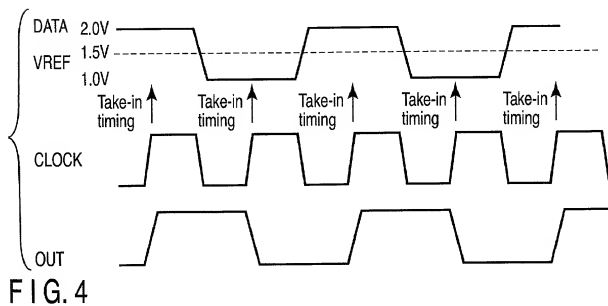
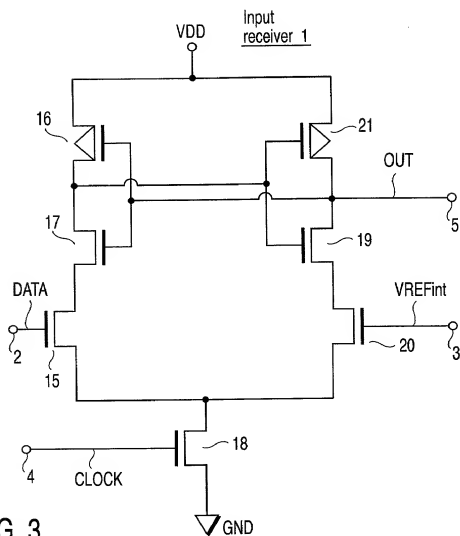


FIG. 2



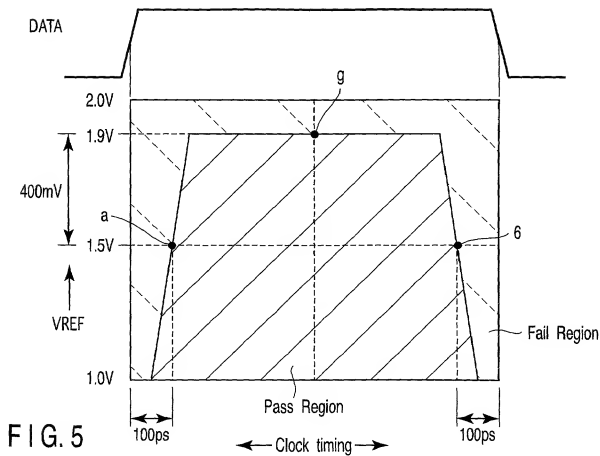


FIG. 5

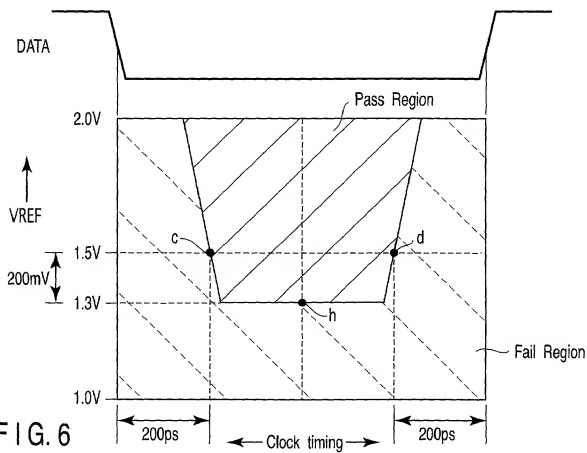


FIG. 6

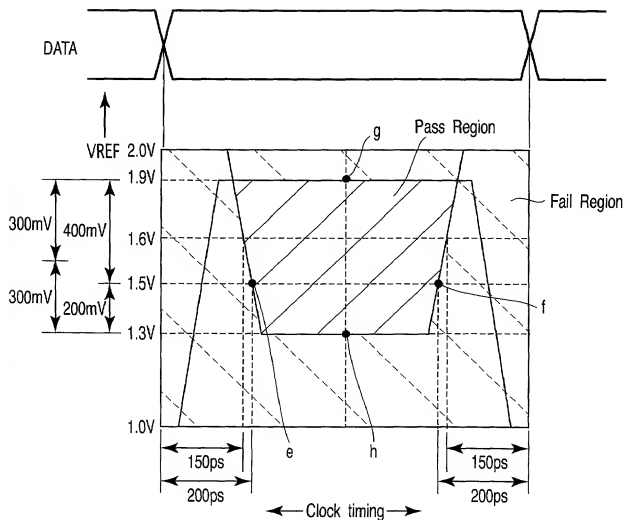


FIG. 7

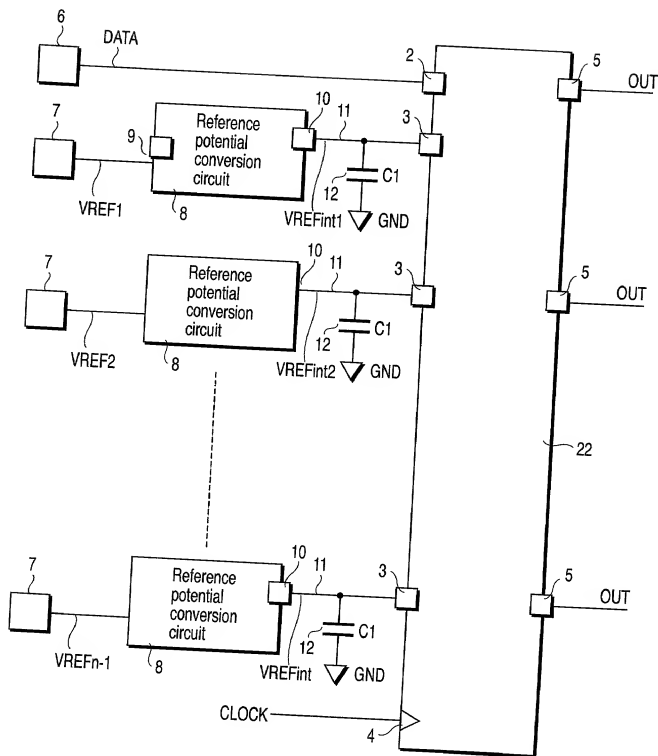


FIG. 8

Reference
potential
conversion
circuit

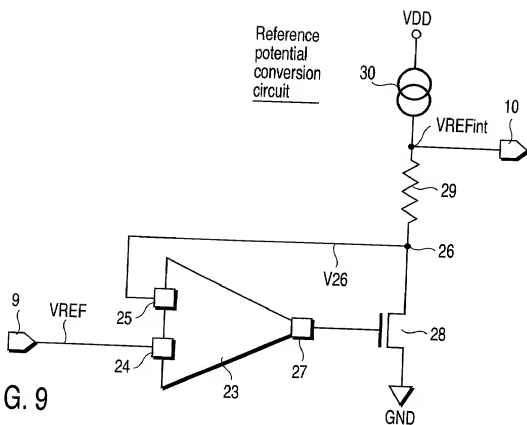


FIG. 9

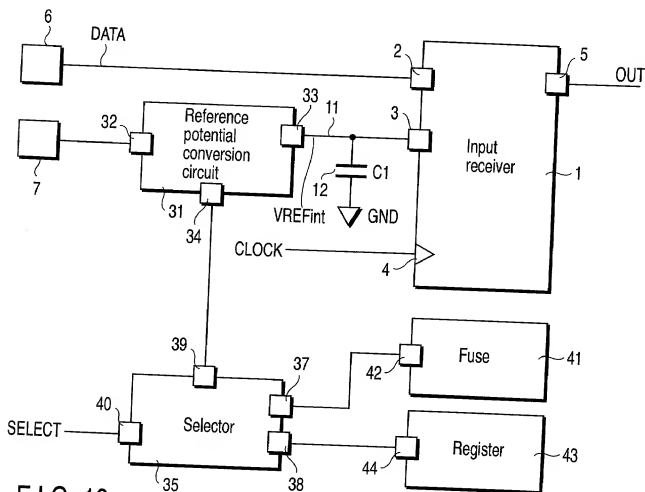


FIG. 10

[illegible]

FIG. 11

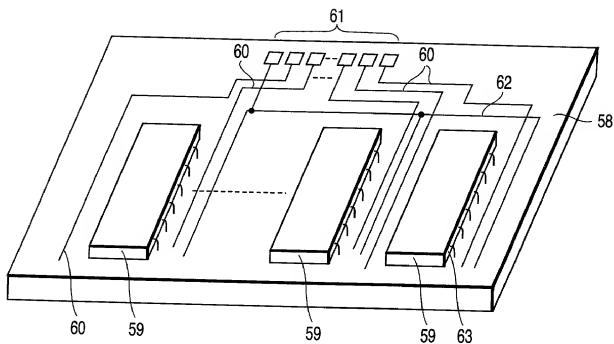


FIG. 12

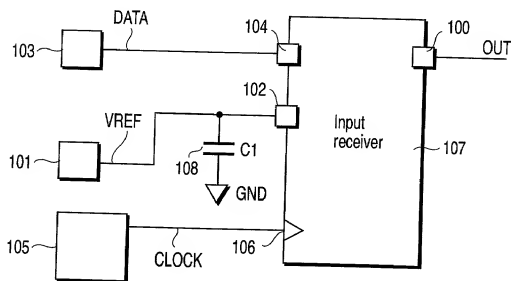


FIG. 13